

Terry Lawell

After serving in the Air Force for 4 years and working at a couple of large computer companies over the next 12 years, I joined Motorola Semiconductors Austin in 1978 as a Product Planning Engineer in marketing for the Microprocessor Division.

In 1979 I joined the Microprocessor design group as a Systems Designer. While at Motorola I was involved with various design projects including 6801, 6809, and the 88K Risc Microprocessor. I was also involved with the R&D of computer architectures for Neural Networks and very high performance Vector Processors. I stayed with Motorola until the end of 2000(with a short break to AMD).

I hold 7 patents from Motorola in communications and microprocessor architecture. I also hold 3 patents from AMD in communications architecture. At the end of 2000 I left Motorola and joined Cadence Design Systems as a Group Director and consultant for computer modeling for verification.

I left Cadence in 2005 to join Verilab (a Scottish based design verification consultancy) as vice president of Business Development.

After 5 years of very interesting work I finally decided to retire in 2010 and pursue a hobby of restoring classic sports cars. Soon an opportunity came my way that I could not refuse.